

Reginald James Peterson

8004 Runnymede Court Raleigh, NC 27601

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Summary

Over 23 years of product verification experience in many areas of telecommunication. Logical, well-organized and result-oriented software/hardware verification engineer with excellent technical and proven communication skills. Resourceful and adept at establishing and maintaining long term working relationships; skilled in assimilating highly technical product knowledge and clearly conveying information to others. Extensive system knowledge and evaluation of call processing, maintenance, patching, system and product upgrades on a variety of products and protocols.

Technical Knowledge

- | | | |
|------------------------------|--------------------------------|-------------------------------|
| • Voice over IP (VoIP) | • 1-Meg Modem (DSL service) | • GR-303/H.248 |
| • Session Initiated Protocol | • NAT | • Ameritec Traffic Simulators |
| • IP Routing/Switching | • WireShark LAN Sniffer | • Mercury Quality Center |
| • VLAN Implementation | • ATM | • CS1500 Softswitch |
| • Q-tagging | • POTS CallP/Features/Services | • DMS-100– CS2K Succession |

Professional Experience

Network Corporation - Research Triangle Park, NC (1985-Present)

CS1500 –Rural Softswitch Verification Engineer (2007-Present)

- Utilize DMS-100 SIP call processing knowledge to verify CS1500 SIP call processing features.
- Ensure CS1500 Softswitch secure network switch operation using IP security test suite.
- Develop test strategies and test plans for new feature development.

DMS-100 Project Leader/Verification Prime for Upgrade of TDM to Succession Soft Switch (2005-2007)

- Evolved legacy circuit switch to Ethernet based VoIP soft switch as part of large team.
- Upgraded TDM networks to trimodal networks (supporting IP, ATM, TDM).
- Simplified and improved the upgrade procedure with better documentation and newly defined tools by working with documentation, design and test tool groups.
- Developed test strategies and test plans based on Sprint and Verizon requirements.

DMS-100 Project Leader for ENET to MG9K ABI Conversion Program (2003-2005)

- Upgraded peripherals and remotes from ENET to Succession network soft switch – CS2K.
- Simplified and improved the upgrade procedure with newly defined tools, improved static data flow and clearer documentation by working with documentation, design and test tool groups.
- Developed test strategies and test plans based on customer requirements.
- Coordinated test schedules based on software design/tool deliverables and hardware availability.
- Developed alternate success plans to allow forward test progress when issues arose.

DMS-100 Project Leader and Verification Prime for Switched Lines over ATM project (1999-2003)

- Decided what team members should test based on team member skill sets and project need.
- Developed and tracked verification strategy and project test plans.
- Reported project status at weekly Switched Lines project review, representing the verification team.
- Conducted lab demos for executives and others.
- Increased knowledge and developed skills on ATM (Passport 15K) via classes and hands-on lab infrastructure development and experience (UNI signaling, VPI, VCI, LANE, in band management).

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DMS-100 Verification Prime and Project Leader for 1-Meg Modem Product (1997-1999)

- Coordinated and developed lab infrastructure to test voice (DMS-100 services) and data throughput/robustness (IP).
- Co-designed the 1-Meg Modem Future Forum presentation that was used for all customer demonstrations. The demonstrations utilized Web access with animation, 3-dimensional modeling, and imbedded GIFs.
- Generated 1-Meg Modem features based on marketing and customer input and product knowledge.
- Presented and supported demonstrations for customers including the Premier of Tasmania, NYNEX, BellSouth, PAC BELL, GTE, and NORTEL Executives.
- Developed, documented and executed verification plan and test cases based on customer requirements and commercial specification

DMS-100 SMA/ESMA/Accessnode Senior Engineer (1989-1997)

- Designed, documented and executed product test plans for ESMA and Accessnode features.
- Communicate the customers' issues to management and designers.
- Consulted on feature designs from an overall end-to-end system view.
- Provided technical support and design suggestions to software design.
- Assisted with various other aspects of Accessnode when customer issues were identified (requested by name).
- Taught internal classes in basic call processing translations and Accessnode Overview.
- Participated as panel member for 1996 Technical Symposium.
- Consulted on ISO documentation for the SMA2 product verification.

DMS-100 RCC2, RCC, XLCM Verification Engineer (1988-1989)

- Coordinated multisite XLCM verification.
- Designed, documented and executed product test plans for various features.
- Generated TATS automation test cases using proprietary scripting.
- Consulted on feature designs.
- Assisted with resolving customer issues.

DMS-100 Operator Services / Billing Verification Engineer (1985-1988)

- Designed, documented and executed test plans for Operator Services and Billing system.
- Consulted on feature designs to aid in keyboard ergonomics.

Education

Purdue University, Hammond, Indiana Campus
Bachelor of Science in Electrical Engineering Technology

References available upon request